

Teraspeed IBIS Models

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Teraspeed Consulting Group LLC announces it's first release of the Teraspeed IBIS Library. This library contains over 13,500 IBIS models of over 100 device manufacturers.

The Teraspeed IBIS Library is the only fully qualified and supported library of IBIS models available in the market today. The Teraspeed Modeling Group has over 10 years of experience in developing high quality Signal Integrity models. Whether you are looking for random logic, memory devices, Microprocessors and support chips, communications chips or FPGA's, over 13,500 of them have already been modeled. If Teraspeed doesn't have a model for your part, our modeling consultants can develop a custom model your design application.

In checking and confirming the quality and completeness of our IBIS models, Teraspeed Modeling Group has implemented, and continually refines, a state-of-the-art model development process ensuring our models are correct. The models in the Teraspeed IBIS Library are verified by:

- Inspection for completeness
- Syntax checking using the IBIS golden parser
- Inspection of waveforms
- Loading models into industry leading SI simulators
- Simulating the model

Teraspeed's models are based on measurements of the actual devices. Teraspeed has developed an extensive set of hardware and software used to make the required measurements and reduce those measurements to IBIS models.

The Teraspeed Modeling Group has taken an innovative approach and has the most experienced engineers in the industry committed to solving the problems of IBIS model quality and availability.

Pricing starts at \$4995. Contact Tom Dagostino at 503-430-1065 or tom@teraspeed.com for more details.